

GOVT. POLYTECHNIC, MANDKOLA (PALWAL)

LessonPlan

Name of the Faculty	:	Smt. MONIKA GUPTA
Discipline	:	Computer Engg.
Semester	:	4 th
Subject	:	CO
Lesson plan duration	:	15 weeks (from 6 March, 2023 to 23 June, 2023)

Week	Theory	
	Lecture Day	Topic (including assignments/tests)
1 st Week	1 st	Hardware organisation of computer system Basic Principle: Basic about Computer System
	2 nd	CPU organization: general register organisation
	3 rd	Stack organization
	4 th	Instruction formats: Introduction
Week 2	1 st	three address, two address,
	2 nd	one address, zero address
	3 rd	RISC instruction
	4 th	Addressing modes: Immediate, register
Week 3	1 st	Direct, indirect, .
	2 nd	relative, indexed
	3 rd	CPU Design: Microprogrammed vs hardwired control.
	4 th	CPU Design: Microprogrammed vs hardwired control.
Week 4	1 st	Reduced instruction set computers Reduced instruction set computers
	2 nd	Reduced instruction set computers Reduced instruction set computers
	3 rd	CISC characteristics
	4 th	RISC characteristics,
Week 5	1 st	Comparison between CISC & RISC
	2 nd	Assignment on CPU Organization
	3 rd	Assignment on CPU Design
	4 th	Discussion on unit 1
Week 6	1 st	Memory organization: Basics About Memory
	2 nd	Memory Hierarchy
	3 rd	RAM and ROM chips
	4 th	Memory address map
Week 7	1 st	Memory connection to CPU
	2 nd	Auxiliary memory: Magnetic disks

	3 rd	Auxillarymemory:magnetictapes
	4 th	Associativememory
Week8	1 st	Cachememory
	2 nd	Virtualmemory
	3 rd	Memorymanagementhardware
	4 th	AssignmentonMemoryHierarchy
Week9	1 st	AssignmentonAuxillarymemory
	2 nd	Test
	3 rd	I/Oorganization: BasisInputoutputsystem(BIOS)
	4 th	FunctionofBIOS
Week10	1 st	Testingandinitialization
	2 nd	Configuringthesystem
	3 rd	AssignmentonBIOS
	4 th	ModesofDataTransfer
Week11	1 st	ProgrammedI/O
	2 nd	Synchronous,asynchronousandinterruptinitiated
	3 rd	Synchronous,asynchronousandinterruptinitiated
	4 th	DMA datatransfer
Week12	1 st	AssignmentonmodesofData Transfer
	2 nd	Test
	3 rd	Architectureofmultiprocessorsystems: Introductionabout Multiprocessorsystems
	4 th	Architectureofmultiprocessorsystems
Week13	1 st	Formsofparallelprocessing
	2 nd	Parallelprocessingandpipelines
	3 rd	Basiccharacteristicsofmultiprocessor
	4 th	AssignmentonmultiprocessorSystem
Week14	1 st	Generalpurposemultiprocessors'
	2 nd	Interconnectionnetworks: timesharedcommonbus
	3 rd	multiportmemory
	4 th	crossbarswitch
Week15	1 st	multistageswitchingnetworksandhypercubestructures
	2 nd	multistageswitchingnetworksandhypercubestructures
	3 rd	AssignmentonInterconnectionnetworks
	4 th	Test